

Call for
Participation

MPSoC'10

10th International Forum on Embedded MPSoC and Multicore

June 28 - July 2, 2010

Nagaragawa Convention Center,
Gifu City, Gifu, Japan



FOCUS

MPSOC is a pluridisciplinary forum bringing together key R&D actors from the different fields required to design Embedded multiprocessor SoC (MPSoC) and Multicore SoC. The full week format and the quality of both attendees and speakers make MPSOC a unique occasion for executives and senior managers to explore new ideas and refine strategic thinking.

CONTENTS

The program brings together key actors from IP, fabless, semiconductor, system houses and design industry to build a vision of the next step in integrated system design. More than 50 world class R&D speakers will discuss **fundamental and strategic issues to master Embedded MPSoC and Multicore design**. The program includes keynotes, 5 days to present strategic directions and state-of-the-art research including presentations by top executives to discuss challenges and opportunities for future SoC applications and technologies.

WHY ATTEND

- MPSoC features an excellent technical program, with lots of interactions, in a great setting. The combination of people, content and setting allows for **more relationships and ideas being formed in a few days than in a month of doing anything else**. The confluence of academic and industrial perspectives, hardware and software perspectives, IP and system challenges, technology vision and business strategies makes MPSoC unlike other conferences, seminars and exhibitions.
- MPSoC is the single best event in the world to bring together so many leading thinkers on the future of SoC design. The technical sessions are presented by **recognized experts** in the field, and with plenty of **time to interact** with these experts. Breadth and depth of the topics are covered. Learning both basic and trendy materials, and **building bridges among different technical areas and corporations/institutes/countries**.
- MPSoC enables great informal networking with prominent experts in multiprocessor SoCs, from academia as well as industry. The best thing about MPSoC is the range of experienced and distinguished researchers that could meet at the same place at the same time, sharing their knowledge with others. **Anyone who wants to share knowledge with researchers and key managers from industry in a unique environment should attend MPSoC.**

FEE

For registration before May 10, the fee amounts to 1250 USD for regular attendees, 1000 USD for IEEE and EDAA members, and 750 USD for students. It will cover the documentation, the lunch for five days, dinner for four days including the social dinner. 2 days registration before May 10 amounts to 750 USD.

Sponsored by

European Design and Automation Association, IEEE Council on Electronic Design Automation*, and IEEE Circuits and Systems Society*

General Co-Chairs:

Yoshinori Takeuchi, Osaka University, Japan
Hiroyuki Tomiyama, Nagoya University, Japan

Local Organization Chair:

Hiroyuki Tomiyama, Nagoya University, Japan

Finance Chair:

Tohru Ishihara, Kyushu University, Japan

Program Chair:

Koji Inoue, Kyushu University, Japan

Technical Program Committee:

Kiyoung Choi, Seoul National University, Korea
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Rolf Ernst, TU Braunschweig, Germany
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Ahmed Jerraya, TIMA, France
Heinrich Meyr, RWTH Aachen University, Germany
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Rainer Leupers, RWTH Aachen University, Germany
Gabriela Nicolescu, Polytechnique Montreal, Canada
Frederic Petrot, TIMA, Grenoble, France
Pierre Paulin, STMicroelectronics, Canada
Chris Rowen, Tensilica, USA
Hannu Tenhunen, Swedish Royal Academy, Sweden
Kees Vissers, Xilinx, USA
Norbert Wehn, University of Kaiserslautern, Germany
Drew Wingard, Sonics, USA
Marilyn Wolf, Georgia Institute of Technology, USA
Hiroto Yasuura, Kyushu University, USA
Yervant Zorian, Virage Logic, USA

Steering Committee:

Ahmed Jerraya, CEA-LETI, MINATEC, France
Hannu Tenhunen, Swedish Royal Academy, Sweden
Marilyn Wolf, Georgia Institute of Technology, USA
Masaharu Imai, Osaka University, Japan
Hiroto Yasuura, Kyushu University, Japan
* pending

REGISTRATION, ACCOMMODATION, VENUE, and FURTHER INFORMATION

Yoshinori Takeuchi, Osaka University, Japan – Email: mpsoc10@vlsilab.ics.es.osaka-u.ac.jp

Further information is available via:

<http://www.mpsoc-forum.org/>

KEYNOTES

- Marco Cornero, STEricsson, Italy
- Kazuo Kajimoto, Panasonic Corporation, Japan
- Pieter Mostermann, Mathworks, USA
- Naoki Nishi, NEC Corporation, Japan
- Jae Cheol Son, Samsung Electronics, Korea
- Sebastian Steibl, Intel, Germany

IN-DEPTH PRESENTATIONS

- Kees van Berkel, STEricsson, France
- Youngchul Cho, Samsung Electronics, Korea
- Yves Dodo, STMicroelectronics, France
- Martti Forsell, VTT, Finland
- Hideaki Ishihara, Denso, Japan
- Tuomas Jarvinen, STEricsson, France
- David Kleidermacher, Green Hills Software, USA
- J Mark Gavin Kokes, Sony-Ericson Mobile Communications, USA
- Tadahiro Kuroda, Keio University, Japan
- Chong-Min Kyung, KAIST, Korea
- Rudy Lawereins, IMEC, Belgium
- Hsien-Hsin Sean Lee, Georgia Institute of Technology, USA
- Young-Long Lin, National Tsing Hua University, Taiwan
- Emil Matus, Technical University Dresden, Germany
- Takashi Miyamori, Toshiba Corporation, Japan
- Ian O'connor, Ecole Centrale de Lyon, France
- Sri Parameswaran, University of New South Wales, Australia

- Martin Schoeberl, Technical University of Denmark, Denmark
- Takahisa Suzuki, Fujitsu Labs, Japan
- Yankin Tanurhan, Virage Logic, USA
- Yasuhiro Tawara, Renesas Technology, Japan
- Sunao Torii, NEC Corporation, Japan
- Kunio Uchiyama, Hitachi, Ltd., Japan
- Kees Vissers, Xilinx, USA
- Zhiyi Yu, Fudan University, China

MINI-KEYNOTES

- Mostapha Aboulhamid, Université de Montréal, Canada
- David Atienza, EPFL, Switzerland
- Lars Bauer, University of Karlsruhe, Germany
- Guy Bois, Ecole Polytechnique Montréal, Canada
- Soo-ik Chae, Seoul National University, Korea
- Kiyong Choi, Seoul National University, Korea
- Thierry Collette, CEA-LIST, France
- Marcello Coppola, STMicroelectronics, France
- Rolf Ernst, Technical University of Braunschweig, Germany
- Olivier Franza, Intel, USA
- John Goodacre, ARM, UK
- Kees Goosens, NXP, Netherlands
- Omar Hammami, ENSTA, France
- Reiner Hartenstein, TU Kaiserslautern, Germany
- Koji Inoue, Kyushu University, Japan
- Tohru Ishihara, Kyushu University, Japan
- Tsuyoshi Isshiki, Tokyo Institute of Technology, Japan
- Charlie Janac, Arteris, USA
- Ahmed Jerraya, CEA-LETI MINATEC, France
- Joachim Kunkel, Synopsys, USA
- Jenq-Kuen Lee, National Tsing-Hua University, Taiwan
- Suzanne Lesecq, CEA-LETI MINATEC, France
- Jan Mandsen, Technical University of Denmark, Denmark
- Vijaykrishnan Narayanan, Pennsylvania State University, USA
- Gabriela Nicolescu, Ecole Polytechnique de Montréal, Canada
- Frédéric Pétrot, TIMA-SLS, France
- Katalin Popovici, Math Works, France
- Ulrich Ramacher, Infineon, Germany
- Chris Rowen, Tensilica, USA
- Benjamin Carrion Schafer, NEC Corporation, Japan
- Yoshinori Takeuchi, Osaka University, Japan
- Hiroyuki Tomiyama, Nagoya University, Japan
- Norbert Wehn, University of Kaiserslautern, Germany
- Pieter Van der Wolf, NXP, Netherlands
- Yuan Xie, Pennsylvania State University, USA
- Koichiro Yamashita, Fujitsu Labs, Japan
- Sungjoo Yoo, Postech, Korea

More talks are planned

MPSOC'10 FORUM REGISTRATION INFORMATION

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ADVANCE REGISTRATION (Before May 10, 2010)

	Regular attendee	IEEE/EDAA member	Student	Speaker	2 Days
Advance Registration	1250 USD	1000 USD	750 USD	750 USD	750 USD
After May 10	1500 USD	1500 USD	1250 USD	1250 USD	1250 USD

REFUNDS: Requests for refunds received before May 10, 2010 will be subjected to a \$50 processing fee. No refunds will be made for cancellations received after May 10, 2010 and all registration fees will be forfeited. Register early to avoid disappointment.

Registration and hotel reservation pages are now open.
Please visit the following URL and register today.

<http://www.mpsoc-forum.org/>