## MIMOSA: Multi-sensor navigation platform Marc POLLINA - CEO





## **Context**

A GNSS/SLAM platform is developed in the frame of a National projet co-financed by DGCIS (MIMOSA BGLE).

This development is also conducted in the frame of common laboratory between M3 Systems and CEA LIST.



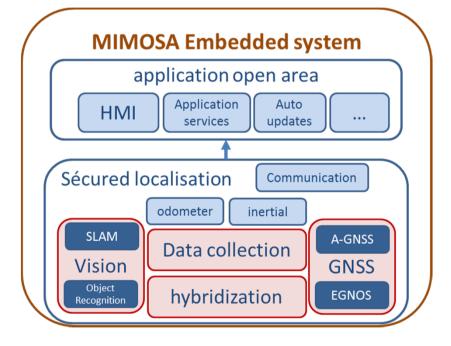
A prototype integrating GNSS signal processing (GPS, GALILEO) and SLAM (visual odemetry based on single camera) will be available for demonstration in Septembre 2014



# **Applications**

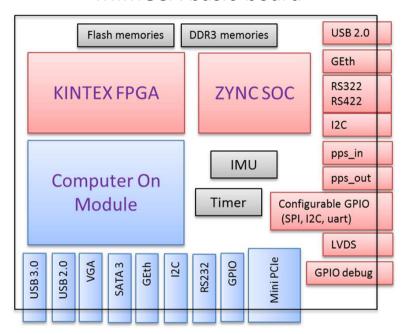
The objective is to integrate on a « real time on-board architecture » the GNSS signal processing , the SLAM image processing and hybridization algorithms (GNSS, vision , IMU,..) in order to achieve robust and accurate positionning in constrained environment

(ex : urban area)



### A given MIMOSA platform configuration is composed with one or several basic-boards

#### MIMOSA basic-board



### Hardware Resources:

**ZYNQ SOC (XILINX)** 

KINTEX FPGA (XILINX)

COMPUTER ON MODULE (KONTRON)

Intel® Core™ i3/ Celeron

Chipset Intel® Mobile QM77/HM76

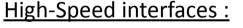
Graphics Controller Intel® HD Graphics 4000

Main Memory: Up to 2x8GB DDR3

Operating system: M3S custom linux distribution

**IMU** sensor

Timer module



PCle Gen2 & Mini-PCle

Gigabit Ethernet

USB3.0 & USB2.0

SATA3.0

LVDS

### <u>Low-Speed interfaces:</u>

RS-232 & RS-422

SPI & 12C

Configurable GPIOs

PPS-in, PPS-out



Wifi, Bluetooth MPSOC – M**&**∮**R§/**3**G**roject



## **Generic board**

### **Board Dimensions:**

6U Europe form factor (16x23cm)

#### FPGA:

- Multiconstellation GNSS receiver
- Image processing accelerator

#### **SOFTWARE**

- Sensors acquisition & datation
- Sensors Hybridization & position calculation



# Demonstrator (sept 2014)



**Demonstrator platform** 

**Application demonstrator** 

# Next steps

### **Industrialisation of:**

- FPGA board
- GNSS IP
- SLAM IP
- Hybridization algorithms

**Full scale application** 





